# *Firmware History*

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| **Subject:** | FW V3.50.2 for SPEED7 CPUs | **Author:** | Erich Heumann |

## History Firmware 3.50.2 for SPEED7 CPUs

Firmware V3.50.2 for SPEED7 CPUs 31x-xxxxx is available for download from the FTP server with immediate effect. The firmware is compatible to its previous versions.

**Changes for CPUs**

1. Length limit of anypointer of 240 Byte at reading data record with SFC 58 resp. SFC 52 was annulled. Now bigger pointers can be indicated.
2. SFC 30 resp. SFC31 can now be cyclically called without loss of time alarms.
3. With SFC20 data can now be read from SDB and L-Stack.
4. Data blocks filed as proctected cannot be overwritten by empty data blocks (S7 code not included). Saved data block headings do not cause overwriting.
5. Ethernet Routing for telegrammes > 960 Byte is now possible. It is necessary for routing to a panel.
6. Routing via Ethernet now also possible with CPUs without DP-Master (CPU312 resp. CPU313).
7. OBs can be filed as protected and processed.
8. CP341S (2 serial interfaces) is supported at the SPEED-Bus.
9. New Siemens programming adapters can now be used which require processing via 7 credits.
10. Timer and counter up to 2048 can now be remanently operated via GSD file. This applies only to CPUs projected as 318 from Siemens.
11. In case error E030 occurs in the process image, the specific cause will now be recorded in the diagnostic buffer.
12. For the global data communication the number of GD circles was increased to 16.
13. A CRC check was introduced at firmware update and stability of firmware update was considerably increased.
14. SF LED lights in case BusInit delivers errors.
15. Now 1024 Byte local data are available per process level.
16. A new diagnostic buffer entry EA08 resp. EA09 occurs in case internal mapping ranges of IOs do not accord with the total IO length at SPEED-Bus modules. The CPU nevertheless starts up.
17. The four-digit product version is indicated on the WEB page of the CPU.
18. CRC check possible during download of blocks. This function can be activated via MMC-CMD. This function is switched off at default. CRC activation for blocks is displayed on the WEB page.
19. Load memory is always set to the maximum for the CPU. This means at a 315-2AG10 it is 1MB and at a 313-5BF03 it is 512KB.
20. At reading out the MMC serial number via SZL it is structured as follows: MMCxxxx.
21. Data indications of a block can now be read with SZL 0E15h.
22. Correct values are now indicated at SZL 0112h.

23. Correct readout of SZL 4X91h and 4X92h. These SZL refer to external DP master systems resp. modules at an external DP master system.
24. Dragging resp. failure of a SPEED-Bus module leads to CPU Stop.
25. A CPU projected as slave now also starts up if only outputs resp. inputs are declared.
26. At S7 connections the error bit is no longer set at status 11.
27. At Profibus-CAN Gateway diagnosis is now indicated correctly.
28. At CP341 an offset for the address can now be indicated at project set-up.
29. An online diagnosis is now also possible at SPEED-Bus modules.
30. At wrong parameterization of a SPEED-Bus module the subsequent modules are now also being parameterized.

**Changes for CP343**

1. S7 connections can now be defined via IP-CONFIG (FB 55).
2. Unspecified TCP and ISO on TCP connections can now be defined via IP-CONFIG (FB 55).
3. FC 10 AG-CTRL has been expanded by functions 6 and 7. A TCP connection is disconnected with function (CMD) 6 and with function 7 the connection is setup again.

**Changes for Profibus**

1. At DPV1 Slaves read/write data record and alarm receipt can now be set separately.
2. SF-LED lights at DP-Slave project engineering in case bus error or diagnosis are identified.

**Changes for ECO-CPU**

1. ECO-CPU 314-2BG03 answers as Siemens 315-2AG10. Maximum 32 modules can be connected to V-Bus.
2. ECO-CPU 314-2BG03 can also be project engineered as 315-2AF03.
3. 2nd interface of ECO-CPU 314-2BG03 is default set as DP-Slave.
4. The process levels of ECO-CPU 314-2BG03 have been adjusted to Siemens 315-2AG10.
5. The CPU can now also be configured as DP-Slave via GSD file.

**Changes for IBS-Master**

1. SPEED-Bus supports double IBS-Master.
2. Consistent reading of data for IBS-Master has been integrated into SFC 254.

**Changes in alarm processing**

1. SFC 105 – SFC 108 integrated for alarm processing.
2. Integration of alarm SFBs 31, 33, 34, 35 and 36. At Siemens these blocks are only available for the 400.